



MCC13 WORKSHOP

<http://www.hh.se/mcc13>

Sixth Swedish Workshop on Multicore Computing
November 25-26, 2013, Halmstad, Sweden

Organisation

General chairs:

Tomas Nordström, Halmstad University, Sweden

Zain-ul-Abdin, Halmstad University, Sweden

Program committee:

Parosh Aziz Abdulla, Uppsala University, Sweden

Peter Brauer, Ericsson AB, Sweden

Mats Brorsson, Royal Institute of Technology, Sweden

Håkan Grahn, Blekinge Institute of Technology, Sweden

Sverker Holmgren, Uppsala University, Sweden

Jorn W. Janneck, Lund University, Sweden

Christoph Kessler, Linköping University, Sweden

Krzysztof Kuchcinski, Lund University, Sweden

Björn Lisper, Mälardalen University, Sweden

Johan Lilius, Åbo Akademi University, Finland

Lasse Natvig, Norwegian University of Science and Technology, Norway

Bertil Svensson, Halmstad University, Sweden

Håkan Zeffner, Windriver, USA

Keynote Speakers:

Prof. Dr.-Ing. J. Teich, Friedrich-Alexander-Universität Erlangen-Nürnberg (FAU), Germany

Prof. Dr.-Ing. Dr. h. c. Jürgen Becker, Karlsruhe Institute of Technology – KIT, Germany

Dr. Benoît Dupont de Dinechin, CTO Kalray, France

Local Arrangements:

Eva Nestius, Halmstad University, Sweden Eva.Nestius@hh.se

Call for Papers

The objective of this workshop is to bring together researchers and practitioners from academia and industry in Sweden and other Scandinavian countries to present and discuss recent work in the area of multicore computing. The workshop is organized as an activity within the network "Swedish Multicore Initiative".

The scope of the workshop is both hardware and software aspects of multicore computing, including design and development as well as practical usage of systems.

Topics of interest include, but not limited to:

- Programming models, frameworks and environments for exploiting parallelism
- Parallel algorithms and applications
- Compiler optimizations and techniques for multi-core systems
- Hardware/software design trade-offs in multi-core systems
- Operating system, middleware, and run-time system support for multi-core
- Correctness and performance analysis of parallel hardware and software
- Tools and methods for development and evaluation of multi-core systems
- Application-specific processors, accelerators and reconfigurable processors
- Architecture of multi-core processors, memory systems, and interconnect and on-chip networks
- Heterogeneous multi-core architectures
- Architectures for emerging technologies and applications
- Power, performance and implementation efficient designs

Participants willing to give a 20 minutes presentation are asked to send an electronic version of a paper presenting original work that does not exceed four pages using the ACM proceedings format as a PDF document (with all fonts embedded, please) through the Submission site:

<https://www.easychair.org/conferences/?conf=mcc13>.

Workshop proceedings with the camera-ready versions of accepted papers will be available at the workshop. Because the proceedings have a small and limited distribution, contributions will not disqualify subsequent publications in conferences or journals.

Important dates

Sep 24 (new date), 2013 Paper submission deadline

Oct 18, 2013 Notification of acceptance

Oct 28, 2013 Camera ready papers due

Nov 11, 2013 Early registration deadline

Nov 25-26, 2013 Workshop